

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	
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<b>Inventor(s) : Patrick GREMILLET</b>	:	Confirmation No.6244
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U.S. Patent Application No. 10/581,549	:	Group Art Unit: 2819
	:	Allowed: May 8, 2008
Filed: June 2, 2006	:	Examiner: Mai, Lam T.
	:	

For:       METHOD OF GENERATING A DIGITAL SIGNAL THAT IS  
              REPRESENTATIVE OF MATCH ERRORS IN AN ANALOG DIGITAL  
              CONVERSION SYSTEM WITH TIME INTERLEAVING, AND AN ANALOG  
              DIGITAL CONVERTER WITH TIME INTERLEAVING USING

**REQUEST FOR ACKNOWLEDGEMENT OF CLAIM OF PRIORITY**  
**UNDER 35 U.S.C. §119(a)-(d) or (f)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

We are in receipt of the Notice of Allowance dated May 8, 2008.

In reviewing the file, it is noted that we have not received acknowledgement of the Claim of Priority as claimed at the time of filing on June 2, 2006 and on March 19, 2007 in the Declaration.

Applicant hereby respectfully requests Acknowledgement of the Claim of Priority Under 35 U.S.C. §119(a)-(d) or (f).

Respectfully submitted,  
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